

LISTING OF CLAIMS

Claims 1-44 are previously cancelled.

45.(previously presented) In an automated data storage library, a system comprising:

a network; and

electronic devices, a plurality of said electronic devices each comprising:

a network interface to said network;

an electronic persistent visual display mounted at said electronic device,
said electronic persistent visual display having an input, said electronic persistent
visual display configured to provide a visual label display which persists
indefinitely, until updated by an input signal at said input;

at least one operational element for operating said automated data storage
library, an operational element for at least one said electronic device comprising
at least one robot accessor; and

a processor configured to operate said at least one operational element:

said processor configured to store information regarding said at least one
operational element and said processor; and

said processor configured to, in response to a predetermined state, provide
an update input signal at said electronic persistent visual display input, said
update input signal comprising selected said information regarding said at least
one operational element and said processor stored by said processor, said update

signal to update said visual label display of said electronic persistent visual display.

46.(previously presented) The system of Claim **45**, wherein said predetermined state of said processor of said at least one electronic device comprises a power-on and/or reset of said electronic device.

47.(previously presented) The system of Claim **45**, wherein said processor of each of said plurality of electronic devices comprises a programmable computer processor and said predetermined state of said processor comprises completion of an update to computer readable program code of said programmable computer processor.

48.(previously presented) The system of Claim **47**, wherein said processor of each of said plurality of electronic devices additionally is configured to update said information regarding said at least one operational element and said processor stored by said processor with status information related to said update to computer readable program code of said programmable computer processor, and said processor update signal selected information comprises at least said status information.

49.(previously presented) The system of Claim **48**, wherein said processor of each of said plurality of electronic devices comprises programmable logic and said predetermined state of said processor comprises completion of an update to said programmable logic.

50.(previously presented) The system of Claim 49, wherein said processor of each of said plurality of electronic devices additionally is configured to update said information regarding said at least one operational element and said processor stored by said processor with a version number of said update to said programmable logic, and said processor update signal selected information comprises at least said version number of said update to said programmable logic.

51.(previously presented) The system of Claim 45, wherein said predetermined state of said processor comprises a state achieved in response to an indication of completion of an engineering change to said electronic device.

52.(previously presented) The system of Claim 51, wherein said processor of each of said plurality of electronic devices additionally is configured to update said information regarding said at least one operational element and said processor stored by said processor with an engineering change number of said engineering change to said electronic device, and said processor update signal selected information comprises at least said engineering change number of said engineering change.

Claim 53 was previously cancelled.

54.(previously presented) The system of Claim 45, wherein said processor of each of said plurality of electronic devices additionally is configured to update said information regarding said at least one operational element and said processor stored by said processor with status information related to said change to said at least one operational element, and said processor update signal selected information comprises at least said status information.

55.(previously presented) The system of Claim 45, wherein said predetermined state of said processor comprises a state achieved in response to a signal received at said network interface.

56.(previously presented) The system of Claim 55, wherein said processor of each of said plurality of electronic devices additionally is configured to select said information stored by said processor in accordance with said signal received at said network interface.

Claim 57 was previously cancelled.